

Docket No.: 03-0195

PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE

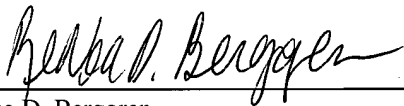
In re Application of: : By the Examiner:
Jeremy Ridgeway, et al. :
Serial No.: : Express Mail Label No. EV 338 284 248 US
Filed: June 19, 2003 : Group Art Unit:

Title: CONFIGURABLE, FAST, 32-BIT CRC GENERATOR FOR 1-BYTE TO 16-BYTES
VARIABLE WIDTH INPUT DATA

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION UNDER 37 C.F.R. §1.10

I hereby certify that this is Information Disclosure Statement and the documents referred to as enclosed therein are being deposited with the United States Postal Service on this date, June 19, 2003 in an envelope as "Express Mail Post Office to Addressee", Mailing Label N° EV 338 284 248 US, with sufficient postage, addressed to: MS Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



ReNea D. Berggren

DATED: June 19, 2003

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98(a)(1).

A copy of each of these items on PTO-1449 is supplied herewith.

Please direct all correspondence and telephone calls to:



24319

PATENT TRADEMARK OFFICE

PETER SCOTT
INTELLECTUAL PROPERTY LAW DEPARTMENT
LSI LOGIC CORPORATION
M/S D-106
1551 MCCARTHY BLVD.
MILPITAS, CA 95035

DATED: June 19, 2003.

Respectfully submitted,
Jeremy Ridgeway, et al.,
LSI Logic Corporation,

By Walter J. Malinowski
Walter J. Malinowski
Reg. N° 43,423

SUITER • WEST PC LLO
14301 FNB PARKWAY, SUITE 220
OMAHA NE 68154-5299
(402) 496-0300 (TELEPHONE)
(402) 496-0333 (TELECOPIER)

In Place of FORM PTO-1449 (Modified)

Serial No.:

Applicant: Jeremy Ridgeway, et al.

Filing Date: June 19, 2003

Group:

Atty. Docket No.: 03-0195

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT**

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA	6,560,742	05/06/03	Dubey et al.	714	757	12/09/99
ABA	6,192,498	02/20/01	Arato	714	781	10/01/98
ACA						
ADA						
AEA						
AFA						
AGA						
AHA						
AIA						
AJA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AKA						
ALA						
AMA						
ANA						
AOA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner

Initial

____ APA Parallel Cyclic Redundancy Check (CRC) for HOTLink™; Cypress Semiconductor Corporation; 03/11/99

____ AQA CRC Tool, Computing CRC in Parallel for Ethernet; Adrian Simionescu, Design Engineer

____ ARA

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.